

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Saito, et al.
Application No.: TBD
Filed: September 10, 2003
For: METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE
Expected Group: 2825
Exp. Examiner: B. Keshavan

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR § 1.97 AND 1.98**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 10, 2003

Sir:

Pursuant to Applicants' duty of disclosure, enclosed please find a List, on a form substantially equivalent to Form PTO-1449, of documents cited in connection with a prior application of the above-identified application, that is, Application No. 10/216,720, filed August 13, 2002. Also enclosed is a copy of Japanese Patent Document No. 2001-345375, and English Abstract thereof, this being one of the references on the enclosed List.

This Information Disclosure Statement is being filed concurrently with the filing of the above-identified application. Clearly, requirements of 37 CFR § 1.97(b) are satisfied.

To the extent that the documents on the enclosed List are not in English, it is respectfully submitted that requirements of 37 CFR § 1.98(a)(3) are satisfied by the

English Abstract enclosed therewith.

In view of all of the foregoing, it is respectfully submitted that all applicable requirements of 37 CFR § 1.97 and § 1.98 have been satisfied, in connection with each of the documents on the enclosed List. Accordingly, consideration of the listed documents, upon examination of the above-identified application, is respectfully requested.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Case No. 648.41969CX1), and please credit any excess fees to such Deposit Account.

Respectfully submitted,

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Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO. 648.41969CX1	SERIAL NO. TBD
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANT G. Saito, et al.	
		FILING DATE September 10, 2003	EXPECTED GROUP 2825

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
AA	6,589,879	7/8/2003	Williams, et al.			
AB	4,693,781	9/15/1987	Leung, et al.			
AC	4,857,477	8/15/1989	Kanamori			
AD	6,235,643	5/22/2001	Mui, et al.			
AE	6,153,494	11/28/2000	Hsieh, et al.			
AF	6,001,706	12/14/1999	Tan, et al.			
AG	5,843,846	12/1/1998	Nguyen, et al.			
AH	6,432,832	8/13/2002	Miller, et al.			

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation/Abstracts	
						Yes	No
AI	2001-345375	12/14/2001	Japan			X	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AJ	S. Wolf, "Silicon Processing for the VLSI Era", Vol. II, Process Integration, 1990, pages 51-54
AK	TI's "Trench Technology Moves in the Factory", Electronics, July 9, 1987, pages 75-87
AL	R. N. Carlile, et al. "Trench Etches in Silicon with Controllable Sidewall Angles", Electrochem. Soc. Solid-State Science and Technology, August 1988, pages 2058-2064
AM	G. K. Herb, et al., "Silicon Trench Etch in a Hex Reactor", Solid State Technology, October 1987, pages 109-115
Examiner	
Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard SI.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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